

ABSTRACT OF THE DISCLOSURE

A control circuit can prevent malfunction of a scan line driving circuit even when any inputted signal is not in normal timing relation due to any cause. The control circuit includes: a counter 11 for counting up to a predetermined count number based on a clock inputted; a comparator arranged at the subsequent stage of the counter to determine whether or not the counter is under counting; and a protection circuit arranged at subsequent stage of the comparator 12 for normalizing a start pulse that starts operation of the scan line driving circuit, even when any input signal of a timing controller is abnormal, by outputting a normalized start pulse 19 via AND gate 18 to which a start pulse 13 generated by the timing controller and an output signal 16 of the comparator 12 are inputted.